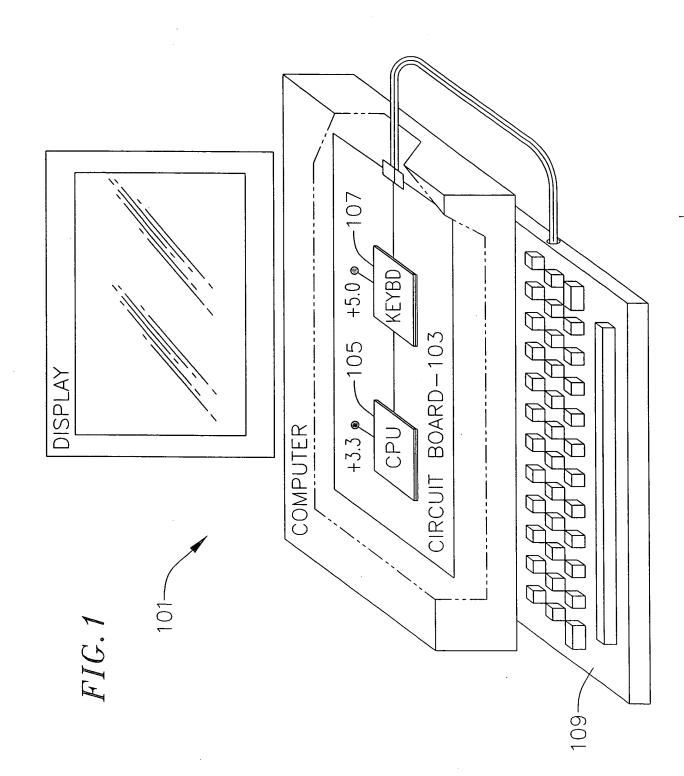
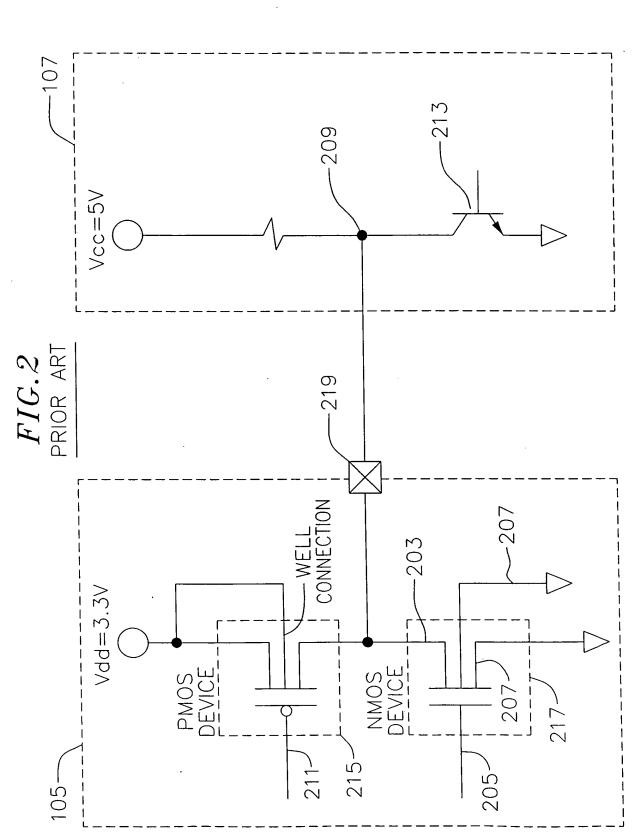


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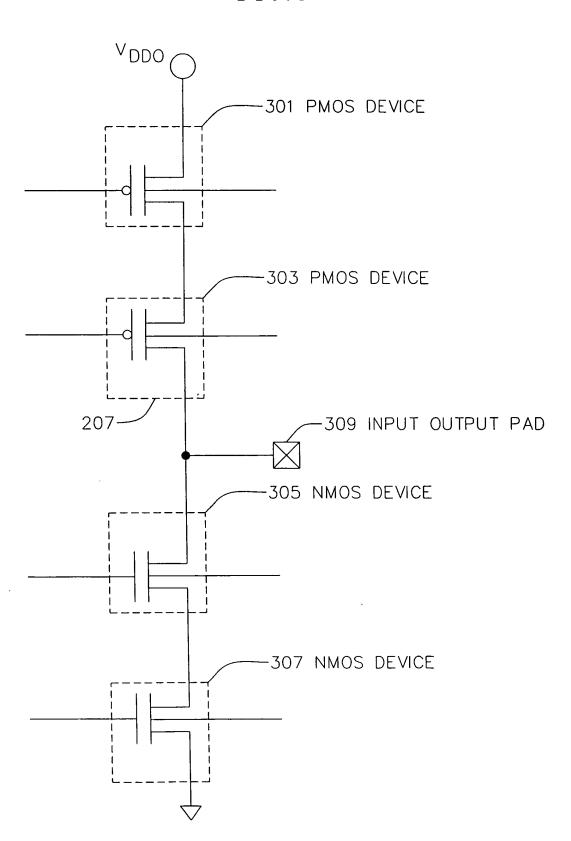
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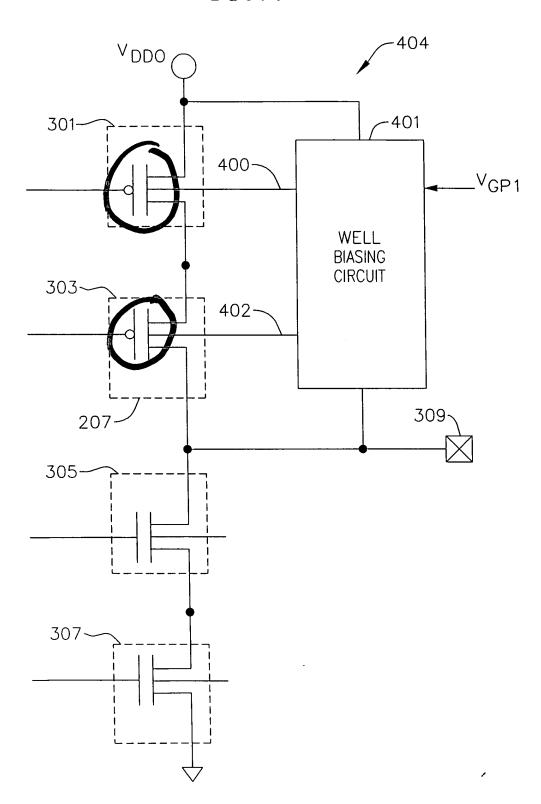
FIG. 3





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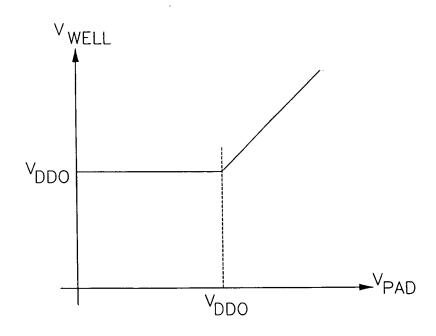
FIG.4





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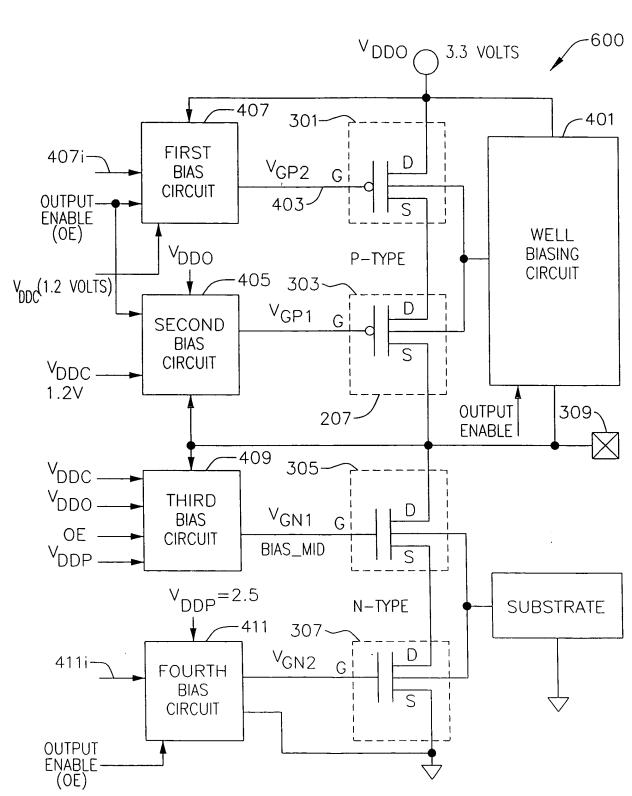
FIG. 5





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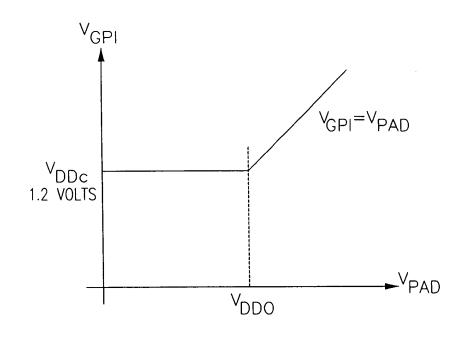
FIG.6





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FIG. 7





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FIG.8

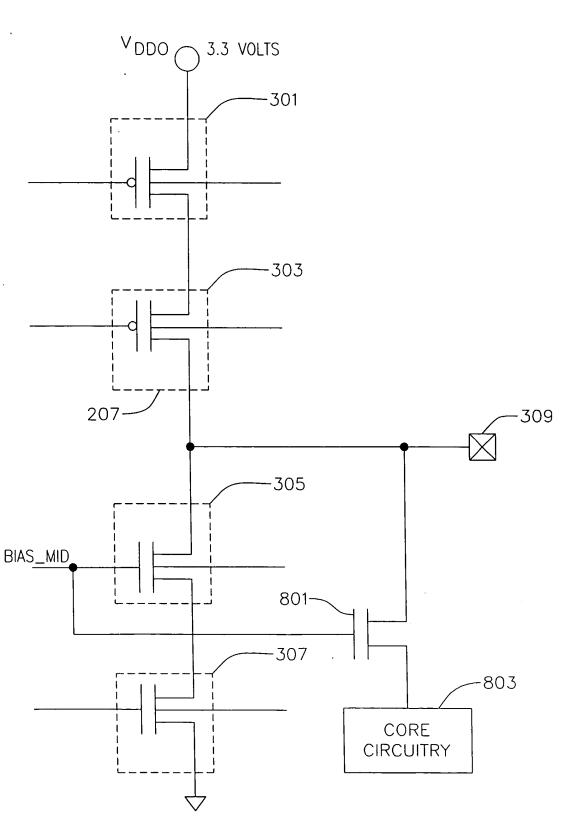
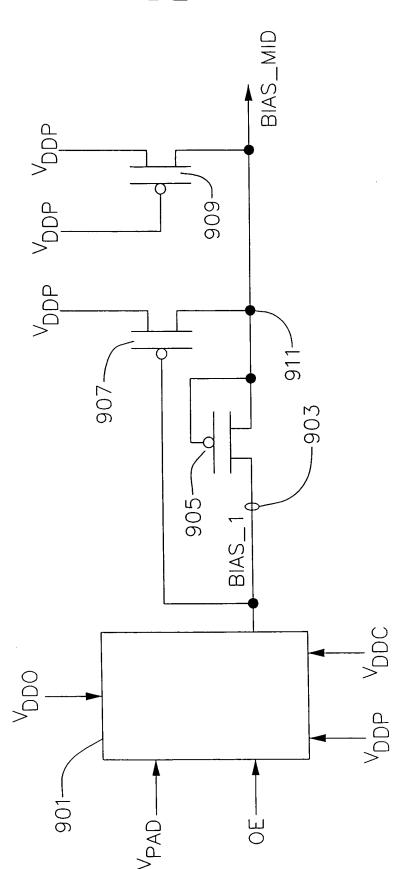




FIG.9A

Application No. 10/043,763
Inventor(s): Janardhanan S. Ajit
Title: SUB-MICRON HIGH INPUT VOLTAGE
TOLERANT INPUT OUTPUT (I/O) CIRCUIT WHICH
ACCOMMODATES LARGE POWER SUPPLY
VARIATIONS

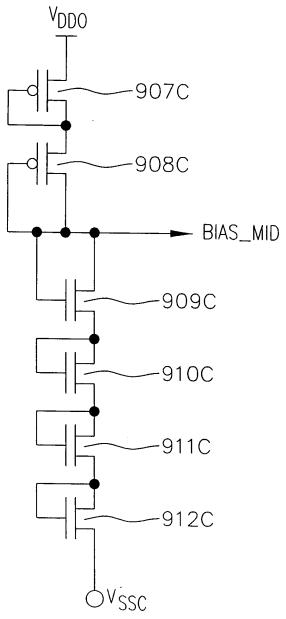
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FIG.9B

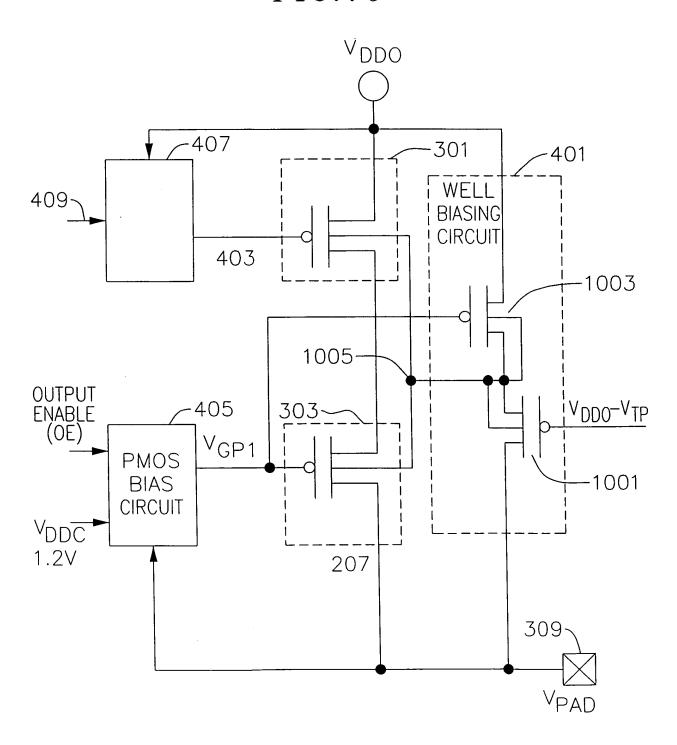


(ALTERNATELY VDDC OR VDDP)



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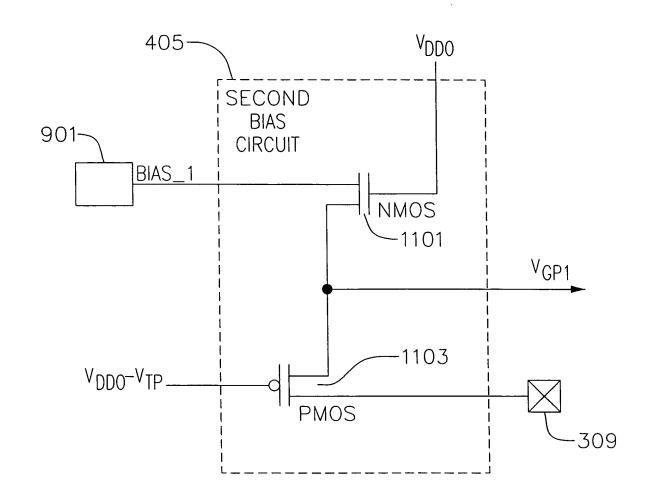
FIG. 10





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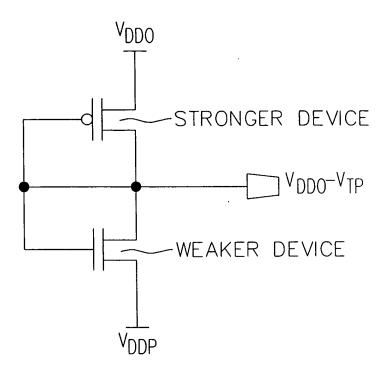
FIG. 11A





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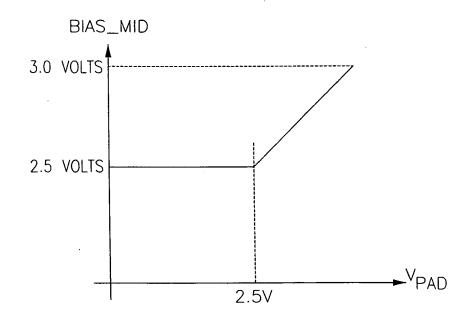
FIG. 11B





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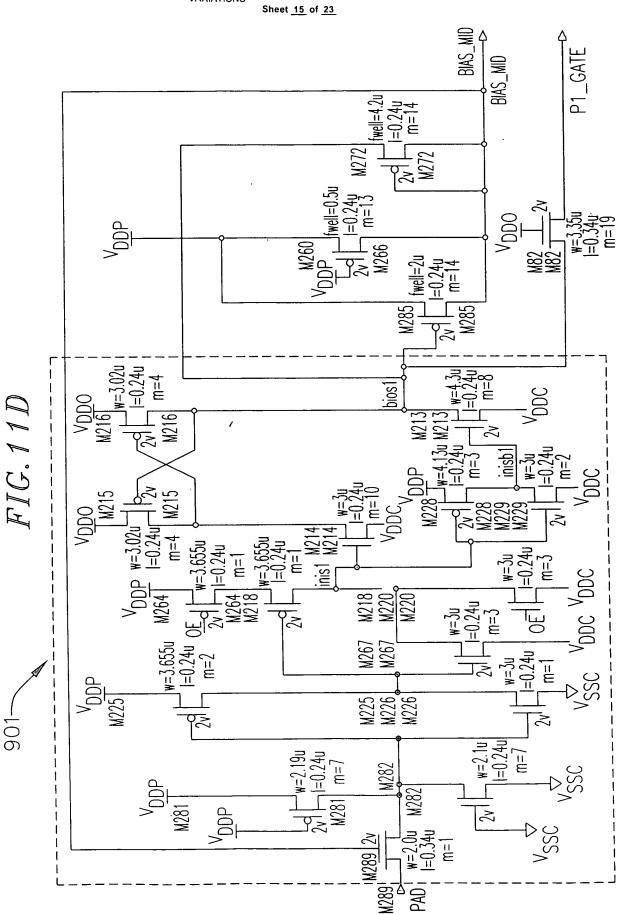
FIG. 11C



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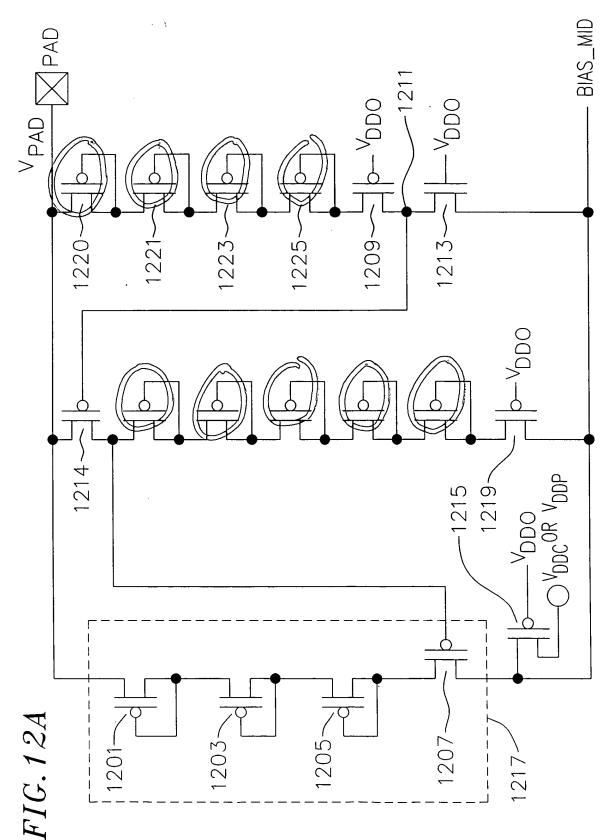


Application No. 10/043,763
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VARIATIONS





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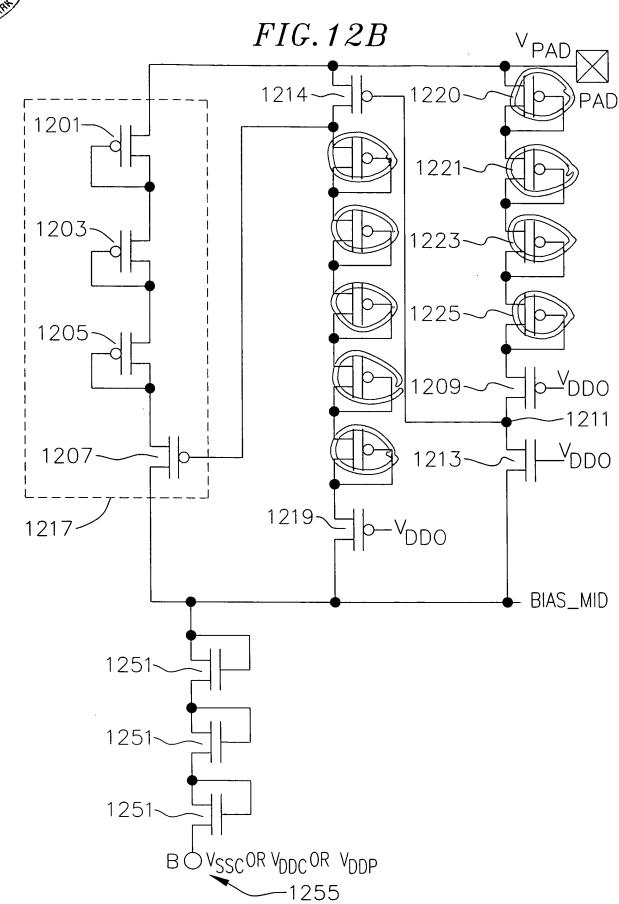


TODATANT ORDER

Application No. 10/043,763 Inventor(s): Janardhanan S. Ajit Title: SUB-MICRON HIGH INPUT VOLTAGE TOLERANT INPUT OUTPUT (I/O) CIRCUIT WHICH ACCOMMODATES LARGE POWER SUPPLY

VARIATIONS

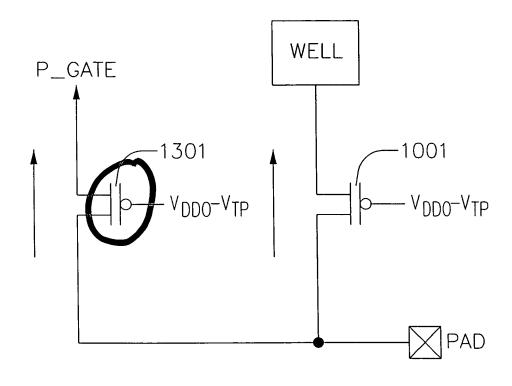
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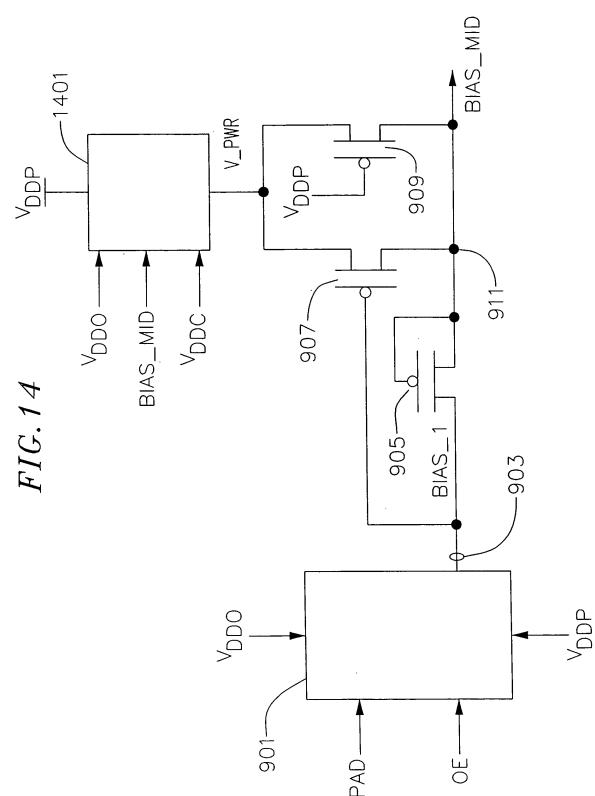
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FIG. 13





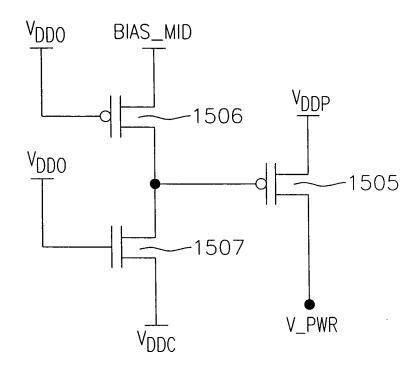
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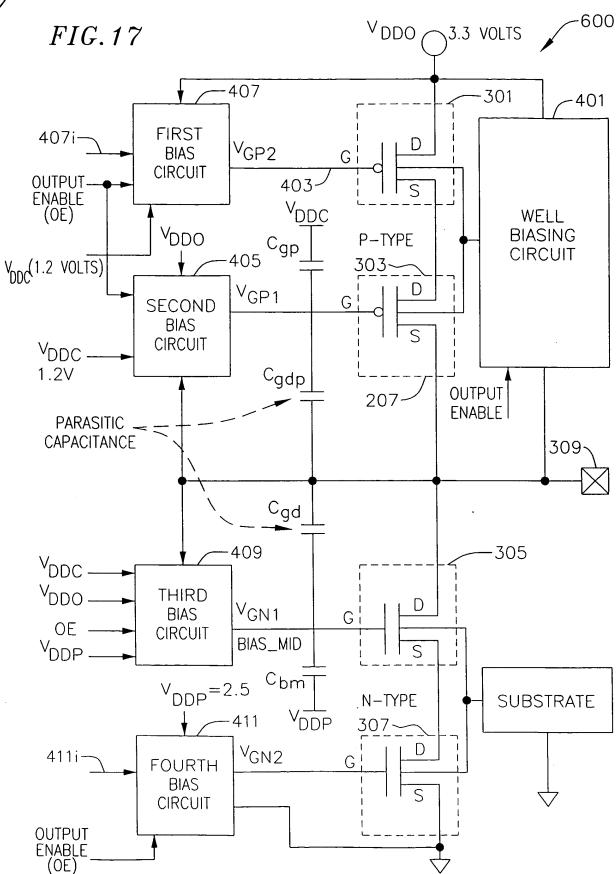
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FIG. 15





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FIG. 18

